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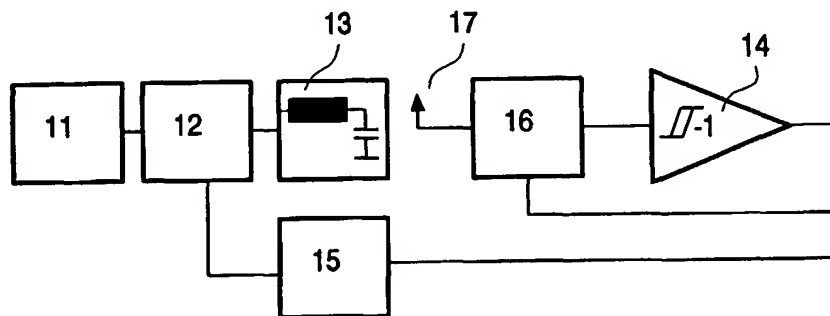
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(54) Title: CIRCUIT AND METHOD OF IGNITING A HIGH-PRESSURE LAMP



(57) Abstract: The invention relates to an electronic circuit for igniting a high-pressure lamp. A resonant circuit 13 of the circuit is used to supply the ignition voltage for the high-pressure lamp. To enable an exact adjustment of the resonance frequency in the resonant circuit, it is proposed that the circuit also comprises converter 12 for generating an alternating voltage with which the resonant circuit 13 is excited and an oscillator 14, 16, 17 for driving the converter 12, the fundamental frequency of the output voltage of the oscillator being at least in proximity to an integral fraction of the resonance frequency of the resonant circuit 13. Finally, it is proposed that the circuit includes a feedback 17 from the resonant circuit 13 to the oscillator 14, 16, 17, whereby the fundamental frequency of the output voltage of the oscillator is so tuned that the resulting frequency of the output voltage of the oscillator corresponds substantially exactly to the integral fraction of the resonance frequency. The invention also relates to a corresponding lighting installation and a corresponding method.

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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

CIRCUIT AND METHOD OF IGNITING A HIGH-PRESSURE LAMP

The invention relates to an electronic circuit and a method of igniting a high-pressure lamp, in particular a UHP (ultra high performance) or HID (high intensity discharge) lamp. The invention also relates to a lighting installation.

High-pressure lamps are known from prior art and are used, for example, in projectors. For ignition they require a short-time high voltage of up to several kilovolts. The required ignition voltage is therefore markedly higher than the normal operating voltage of the lamp and of its electronic system.

It is also known from existing practice that resonant circuits are used to ignite HID and UHP lamps. These circuits achieve a sufficiently high increase in the electronic operating voltage by exciting the resonance frequency of the resonance circuit. The high increase in voltage attainable with a resonant circuit when excited with a square-wave voltage characteristic having a frequency corresponding to the resonance frequency of the resonant circuit is expressed by

$$\frac{V_{\max}}{V_{DC}} = 1 + \frac{2}{\pi} \cdot Q.$$

In this equation V_{DC} is the operating voltage and Q the quality factor of the resonant circuit.

US 6,160,362 describes an electronic circuit for igniting a high-pressure lamp in which a resonant circuit is supplied with voltage for example, via a bridge circuit. The bridge circuit is switched by a control circuit, e.g. a microcontroller. It is proposed that the frequency of the voltage made available by the bridge circuit is first started far above the resonance frequency. The voltage across the lamp is detected and the frequency is reduced until the voltage required for ignition is reached. It is claimed that in this way the circuit is always operated at most at the voltage required for ignition. With this approach, however, it must be ensured that the frequency of the voltage supplied by the bridge circuit does not fall below the resonance frequency. Otherwise there is a danger that components could be damaged by excessively high currents. To ensure that the minimum required voltage is

reached and that the frequency is not reduced below the resonance frequency, overdimensioning of the circuit is therefore required because of component tolerances.

Advantageously, the resonance of the oscillating circuit is not excited with a square-wave voltage characteristic having a frequency corresponding to the resonance frequency, but with a harmonic of such a square-wave voltage characteristic. In this way higher resonance frequencies can be utilized even when using electronic modules which switch comparatively slowly. A higher resonance frequency results in substantially smaller currents during ignition, so that smaller components can be used.

When harmonics are used, the relationship between the maximum attainable voltage and the operating voltage is calculated by:

$$\frac{V_{\max}}{V_{DC}} = 1 + \frac{2}{k\pi} \cdot Q$$

In this equation k is the ordinal number of the harmonic used. It is clear from the equation that if a harmonic is used to excite the resonant circuit the quality of the oscillating circuit must be correspondingly higher to achieve the same maximum voltage.

For example, if the operating voltage V_{DC} is 400 V and the desired maximum voltage V_{\max} is 5 kV, and if the third harmonic is used, the required quality factor Q equals 54, whereas if an oscillation having the resonance frequency itself were used, Q would equal only 16.

In known electronic circuits for igniting high-pressure lamps the harmonic is made available by means of an oscillator with a fixed set frequency. The output signal of the oscillator then switches a converter, to the output of which a resonant circuit of suitable quality is connected.

However, the quality of the resonant circuit also influences the utilizable bandwidth of the resonant circuit. If the resonance frequency is not exactly matched, the voltage attained is less than the maximum attainable voltage. A relative frequency error of $1/2Q$, that is, of 0.9% in the above example, is sufficient to cause the attained voltage to fall to a value of 71%. Even normal component tolerances produce deviations above this limit.

It is therefore an object of the invention to make possible a frequency control system for the electronic energization of a resonant circuit by means of which the excitation frequency autonomously adjusts itself to a fraction of the resonance frequency of the resonant circuit. An electronic circuit used for this purpose must be robust, must require no

overdimensioning of the components used, and must prevent the frequency from falling significantly below the resonance frequency of the resonant circuit.

This object is achieved according to the invention by an electronic circuit for igniting a high-pressure lamp. The electronic circuit comprises a resonant circuit for providing an ignition voltage for the high-pressure lamp and an converter for generating an alternating voltage with which the resonant circuit is excited. The alternating voltage generated is preferably of square waveform. In addition, the electronic circuit according to the invention includes an oscillator to drive the converter. The fundamental frequency of the output voltage of the oscillator must be at least in proximity to an integral fraction of the resonance frequency of the resonant circuit. Finally, the electronic circuit according to the invention comprises a feedback from the resonant circuit to the oscillator. On the basis of this feedback, the fundamental frequency of the output voltage of the oscillator is tuned in such a way that the resulting frequency of the output voltage of the oscillator corresponds substantially exactly to the above-mentioned integral fraction of the resonance frequency.

A further object of the invention is achieved by a lighting installation which includes, in addition to the proposed electronic circuit, a high-pressure lamp connected to the resonant circuit of the electronic circuit.

This object is likewise achieved according to the invention by a corresponding method of igniting a high-pressure lamp by means of a resonant circuit supplied with an alternating voltage by a converter.

The invention is based on the idea that a feedback from the resonant circuit to the oscillator can be used to tune the frequency of an oscillation to a fraction of the actual resonance frequency. Although the actual resonance frequency of a resonant circuit cannot be precisely predetermined because of component tolerances, such a feedback, which supplies to the oscillator an indication of the frequency and the phase position of the oscillation in the resonant circuit, permits a precise adjustment of the excitation of the resonant circuit.

The fundamental frequency of the oscillator should already be close to an integral fraction of the resonance frequency. Otherwise, because of the feedback, the oscillator could adjust itself to a fraction above or below the anticipated fraction of the resonance frequency. The quality of the resonant circuit, in the case of an optimized circuit, is insufficient for a frequency at a fraction of the resonance frequency of a higher order than anticipated so that the required maximum voltage is not attained. In the case of an optimized circuit, damage to the components of the circuit is to be feared for a frequency at a fraction of

the resonance frequency of a lower order than anticipated since in that case excessive power dissipation occurs.

It is an advantage of the invention that it permits a precise and autonomous adjustment of the harmonic to the resonance frequency. This makes possible a robust electronic circuit, since the integral fraction of the resonance frequency can be selected to be tuned to a desired quality of the resonant circuit and to a selected dimensioning of the components. Alternatively, given a selected integral fraction of the resonance frequency, the quality of the resonant circuit can be limited to the required minimum and overdimensioning of the components avoided.

Advantageous embodiments of the circuit and of the method according to the invention are the subject matter of the dependent claims.

The feedback should be able to measure, as far as possible without losses, the high voltages of, for example, up to 5 kV and to reduce them to a level which is suitable for processing by normal small-signal electronic circuits. At the same time, however, sufficient feedback should be ensured even at relatively low voltages, since the resonant circuit is not initially in resonance and therefore supplies only low voltages of, for example, 400 V.

In a preferred embodiment of the invention, the feedback is therefore effected by means of a capacitive antenna with which the frequency in the resonant circuit can be reliably detected even in the case of large voltage differences. Moreover, a capacitive antenna has low losses and makes the electronic circuit less sensitive to other capacitive influences which could occur at the high voltages.

If the electronic circuit is a printed circuit, the capacitive antenna advantageously takes the form of a suitable conductor track configuration. Such a conductor track configuration is cheaper and more reliable than a capacitance element constructed as a separate unit.

Although the circuit according to the invention allows the resonant circuit to be put reliably into resonance mode, the maximum voltage resulting from resonance cannot be exactly predetermined. The reasons are, firstly, component tolerances and, secondly, external influences such as air humidity. In addition, as the temperature of the resonance coil of the resonant circuit increases, the maximum voltage initially increases. Once a certain voltage is exceeded, however, this can cause damage to the components of the resonant circuit.

In a preferred embodiment of the electronic circuit according to the invention, therefore, the magnetic material of the resonance coil is so selected that it reaches saturation

precisely when a desired voltage is reached. The desired voltage is normally the voltage required for ignition of the high-pressure lamp. In this way it can be ensured that a maximum value for the voltage of, for example, 5 kV is not exceeded. In addition, the saturation of the magnetic material has the advantage that, with rising temperatures, it initially decreases,
5 inversely to the voltage, leading to a compensation of the two effects.

Through the adjustment of the saturation of the magnetic material to a desired voltage, therefore, further voltage control becomes superfluous.

The oscillator of the electronic circuit according to the invention can be realized by means of an analog comparator, but also by means of a digital PLL (phase locked
10 loop). If a PLL is used, both the output of the inverter and the output of the resonant circuit should be captured for the feedback in order to enable on this basis an adjustment to a phase difference of exactly 90° .

These and other aspects of the invention are apparent from and will be elucidated with reference to the embodiments described hereinafter.

15 In the drawings:

Fig. 1 shows a block diagram of a first embodiment of the circuit according to the invention having an analogue comparator;

20 Fig. 2 shows an embodiment of an oscillator of the circuit according to the invention;

Fig. 3 shows the voltages occurring at the oscillator from Fig. 2, and

Fig. 4 shows a block diagram of a second embodiment of the circuit according to the invention having a PLL controller.

25 The block diagram in Fig. 1 illustrates schematically a first embodiment of the electronic circuit according to the invention which is to be used to ignite a high-pressure lamp.

The electronic circuit illustrated includes a direct voltage source 11 which is connected to an inverter 12 which is drivable by means of switching elements. A resonant
30 circuit 13 which is used to supply the ignition voltage for the high-pressure lamp is connected to the output of the inverter 12.

An antenna 17 which takes the form of the open end of a copper surface on the circuit board is used as the detector for the actual resonance frequency and is connected to a comparator 14 via a matching circuit 16. The output of the comparator 14, which is provided

with a hysteresis, is back-coupled to the matching circuit 16. In addition, the output signal of the comparator 14 is fed back to the switching elements of the converter 12 via a delay unit 15.

5 The matching circuit 16 and the comparator 14 together form an oscillator which operates without tuning at least in proximity to an integral fraction of the anticipated resonance frequency of the resonant circuit 13.

The electronic circuit of Fig. 1 may form part of a lighting installation in which a high-pressure lamp (not shown) is connected to the resonant circuit 13.

10 For the frequency control system according to the invention, the antenna 17 captures the voltage characteristic across the resonant circuit and feeds it to the matching circuit 16. Although high voltages in the resonant circuit are captured by the antenna 17 with an attenuation of amplitude because of the capacitive configuration of the antenna 17, the voltage characteristic which is passed on does reflect the frequency in the resonant circuit for each voltage. The matching circuit 16 passes on the voltage characteristic received to the
15 comparator 14 in such a way that the zero transitions coincide exactly in time with the switching of the comparator 14 if the output frequency of the comparator 14 corresponds exactly to an integral fraction of the resonance frequency of the resonant circuit 13.

To achieve this, the phase position of the coupling-in of the antenna signal is so selected that a lag of the zero transition, indicating that the resonance frequency is lower
20 than the actual oscillator frequency, causes a delay of switching by the comparator. By contrast, a lead of the zero transition, indicating that the resonance frequency is higher than the actual oscillator frequency, causes an acceleration of switching by the comparator. The phase position of the voltage signal captured by the antenna 17 is offset by 90° with respect to the signal at the output of the converter 12 if the resulting switching frequency is exactly
25 an integral fraction of the natural resonance frequency of the oscillating circuit.

The output signal of the comparator 14, the switching times of which are therefore precisely tuned to a fraction of the resonance frequency of the resonant circuit 13, is then used to trigger the converter 12 at a fraction of the resonance frequency.

The final phase position of the signal with which the converter 12 is triggered
30 is determined by programming of the delay unit 15 of Fig. 1.

As compared to the high voltage of the resonant circuit 13, the output signal of the comparator 14 has a phase shift of 90° if the oscillator is exactly adjusted to an integral fraction of the resonance frequency. If the oscillator frequency is far above an integral fraction of the resonance frequency, the output signal of the comparator 14 is phase-shifted

by 180° with respect to the high voltage of the resonant circuit 13. If, however, the oscillator frequency is far below an integral fraction of the resonance frequency, the output signal of the comparator 14 is not phase-shifted with respect to the high voltage of the resonant circuit 13.

5 To match the phase of the output signal of the comparator 14 to the phase required at the converter 12 for exciting the resonant circuit 13 with a harmonic of the resonance frequency, the delay unit 15 therefore provides a phase shift of 90° or a corresponding phase shift of $90^\circ + n \cdot 180^\circ$, where n is a natural number. However, phase shifts of more than 270° are less advantageous because of the resulting long delay between
10 measurement and triggering.

To achieve that the delay times unavoidably occurring during processing in the circuit do not prevent exact tuning, the delay unit 15 additionally causes a corresponding compensation of the delay in the signal path from the comparator 14 to the converter 12. This can be achieved in that a fixed phase shift of, for example, 270°, reduced by the previously
15 determined, design-dependent delay times in the circuit, is set in the delay unit 15. The inversion required because of the opposite signs at 90° and at 270° can be effected, for example, in the converter.

Fig. 2 shows a possible configuration of the oscillator formed by the antenna 17, the matching circuit 16 and the comparator 14. The analog oscillator shown in Fig. 2 is
20 used to excite the resonance in the resonant circuit 13 with a harmonic of the resonance frequency having an ordinal number of $k=3$.

In the oscillator circuit a capacitor C32 of 0.1 pF, which represents the capacitive antenna 17, is connected via a resistor R32 to a first input 2 of an operational amplifier 21 functioning as a comparator. The connection between the capacitor C32 and the
25 resistor R32 is connected to ground via a further capacitor C37 of 120 pF. The two capacitors C32 and C37 form a potential divider for the voltage captured by the antenna.

The connection between the resistor R32 and the first input 2 of the operational amplifier 21 is connected on one side to ground via a resistor R20 and on the other to a first supply voltage via a resistor R19. In addition, a further resistor R1 is
30 connected to the output 7 of the operational amplifier 21 via a capacitor C38. The resistors R19, R20 and R1 together with the capacitor C38 produce the hysteresis of the comparator 14. At the same time the capacitor C38 prevents a reaction by direct voltage components, so that the symmetry of the output voltage of the operational amplifier 21 and therefore of the whole oscillator is ensured.

A second supply voltage is fed to the operational amplifier 21.

In addition, the output 7 of the operational amplifier 21 is fed back to the second input 3 of the operational amplifier 21 via a resistor R18, whereby the output frequency of the operational amplifier 21 can be tuned to the resonance frequency of the resonant circuit 13 captured via the capacitive antenna C32. The connection between the resistor R18 and the second input 3 is connected to ground via a frequency-determining capacitor C37 of 100 pF. The voltage across the capacitor C27 is thus applied to the second input 3 of the operational amplifier 21.

As long as no signal is received via the antenna C32, a square-wave output signal of a certain fundamental frequency is produced at the output 7 of the operational amplifier 21. Because of the components of the capacitor C27 and of the resistor R18 used for the hysteresis circuit, the fundamental frequency of the output signal is in proximity to 1/3 of the anticipated resonance frequency of the resonant circuit.

In Fig. 3 the voltages occurring at the oscillator during frequency control are represented in a diagram. In the diagram the voltages concerned are plotted as four curves 31 to 34 in volts against time in μs .

Curve 31 represents the signal captured by the capacitive antenna C32. This signal 31 is composed of a substantially sinusoidal oscillation with an amplitude of approx. 8 V and a direct voltage component of approx. 2.5 V.

Curve 32 represents the signal at the first input 2 of the operational amplifier 21. Because of the potential divider, this signal 32 is, firstly, attenuated as compared to the signal 31 captured by the antenna C32; secondly, superposed on it is an additional, alternating direct voltage component which is produced by the feedback of the output 7 of the operational amplifier 21 via the hysteresis circuit. The whole direct voltage component is switched, approximately after three semioscillations of the high-voltage signal captured by the antenna, from a higher value of approx. 3V to a lower value of approx. 2V and inversely. The value of the additional, alternating direct voltage component is therefore approx. ± 0.5 V.

Curve 33 represents the square-wave output signal of the operational amplifier 21 at output 7, which signal results from the comparison of the voltages supplied to the first input 2 and the second input 3 of the operational amplifier 21. The square-wave output signal 33 alternates between approx. 0 V and approx. 5 V.

Finally, curve 34 represents the voltage fed back from the output 7 of the operational amplifier 21, which has a triangular waveform as a result of the capacitor C27

and is fed to the second input 3 of the operational amplifier 21. The characteristic of the triangular-waveform voltage moves in each wave from below 2 V to above 3 V and back.

Curves 32 and 34 intersect in each case at approximately the zero transition of the voltage 31 captured via the antenna C32, and switching of the operational amplifier 21 is effected at each intersection. The feedback ensures that the switching is shifted exactly to the zero transitions of the signal 31. As soon as the switching occurs exactly simultaneously with the zero transitions of the voltage captured by the antenna 7, the output frequency of the comparator is exactly the desired integral fraction of the resonance frequency of 1/3.

Fig. 4 shows a second embodiment of the electronic circuit according to the invention, based on the use of a PLL.

The circuit of Fig. 4 again comprises a direct voltage source 11 which is connected to a resonant circuit 13 via an converter 12. Likewise, the circuit again includes an antenna 17 as detector for the actual resonance frequency.

In contrast to the first embodiment, however, the output signal of the antenna 17 is here fed to a PLL controller 18. The output signal of the converter 12 is also fed to the PLL controller 18. In addition, the output signal of the PLL controller 18 is fed back directly to the switching elements of the converter 12.

The PLL controller 18 is so dimensioned that a frequency, based on a desired fraction of the anticipated resonance frequency, is produced at its output, at which frequency the inverter output signal and the voltage at the resonant circuit 13 have a phase shift of 90° . This makes compensation of the other delays in the circuit by means of a delay unit unnecessary.

The electronic circuit of Fig. 4 may also form part of a lighting installation in which a high-pressure lamp (not shown) is connected to the resonant circuit 13.

The embodiments described represent only two of various possible embodiments of the invention.

CLAIMS:

1. An electronic circuit for igniting a high-pressure lamp, comprising resonant circuit (13) for providing an ignition voltage for the high-pressure lamp;
a converter (12) for generating an alternating voltage with which the resonant
5 circuit (13) is excited;
an oscillator (14, 16, 17) for driving the converter (12), the fundamental frequency of the output voltage of the oscillator (14, 16, 17) lying at least in proximity to an integral fraction of the resonance frequency of the resonant circuit (13), and
a feedback (17) from the resonant circuit (13) to the oscillator (14, 16, 17),
10 whereby the fundamental frequency of the output voltage of the oscillator (14, 16, 17) is so tuned that the resulting frequency of the output voltage of the oscillator (14, 16, 17) corresponds substantially exactly to the integral fraction of the resonance frequency in proximity to which the fundamental frequency of the output voltage of the oscillator (14, 16, 17) lies.
15
2. An electronic circuit as claimed in claim 1, characterized in that the feedback coupling comprises a capacitive antenna (17) for detecting the output voltage of the resonant circuit (13).
- 20 3. An electronic circuit as claimed in claim 2, characterized in that the capacitive antenna (17) is formed by a conductor track configuration on a printed circuit.
4. An electronic circuit as claimed in any one of the preceding claims, characterized in that a delay unit (15) is contained in the signal path from the oscillator (14,
25 16, 17) to the converter (12).
5. An electronic circuit as claimed in any one of the preceding claims, characterized in that the oscillator (14, 16, 17) comprises an analog comparator (14, 21).

6. An electronic circuit as claimed in claim 5, characterized in that the oscillator (14, 16, 17) has a hysteresis which determines the integral fraction of the resonance frequency in proximity to which the fundamental frequency of the output voltage of the oscillator (14, 16, 17) lies.

5

7. An electronic circuit as claimed in any one of the claims 1 to 4, characterized in that the oscillator comprises a digital circuit, in particular a digital PLL (phase locked loop) (18).

10 8. An electronic circuit as claimed in claim 7, characterized in that the digital circuit (18) utilizes both the signal of the resonant circuit (13) and the output signal of the converter (12) for frequency control.

9. An electronic circuit as claimed in any one of the preceding claims,
15 characterized in that the resonant circuit (13) comprises a resonance coil whose the magnetic material reaches saturation at a predefined voltage, in particular the ignition voltage determined for the high-pressure lamp.

10. A lighting system comprising an electronic circuit as claimed in any one of the
20 preceding claims and comprising a high-pressure lamp connected to the resonant circuit (13) of the electronic circuit.

11. A method of igniting a high-pressure lamp by means of a resonant circuit (13)
supplied with an alternating voltage by an converter (12), the method comprising the
25 following steps:

a) driving of the converter (12) by a voltage which is output by an oscillator (14, 16, 17) such that the converter (12) outputs an alternating voltage, the fundamental frequency of the output voltage of the oscillator (14, 16, 17) lying at least in proximity to a fraction of the resonance frequency of the resonant circuit (13);

30 b) supplying of the alternating voltage output by the converter (12) to the resonant circuit (13) so as to excite an oscillation in the resonant circuit (13);

c) feeding the oscillation caused in the resonant circuit (13) back to the oscillator (14, 16, 17), such that the frequency of the output voltage of the oscillator (14, 16, 17) is adjusted to a value which corresponds substantially exactly to the fraction of the

resonance frequency of the resonant circuit in proximity to which the fundamental frequency of the output voltage of the oscillator (14, 16, 17) lies, and

d) repetition of steps a) to c) until a voltage required for ignition of the high-pressure lamp is attained at the resonant circuit (13).

5

12. A method as claimed in claim 11, characterized in that in step a) the signal output by the oscillator (14, 16, 17) is transmitted with a delay for driving the converter (12).

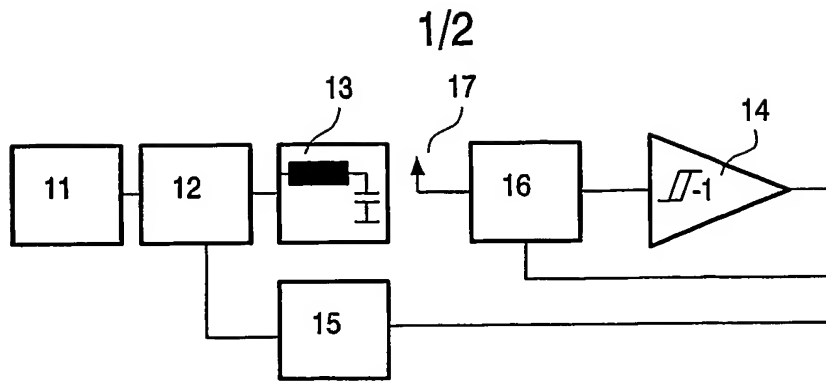


FIG. 1

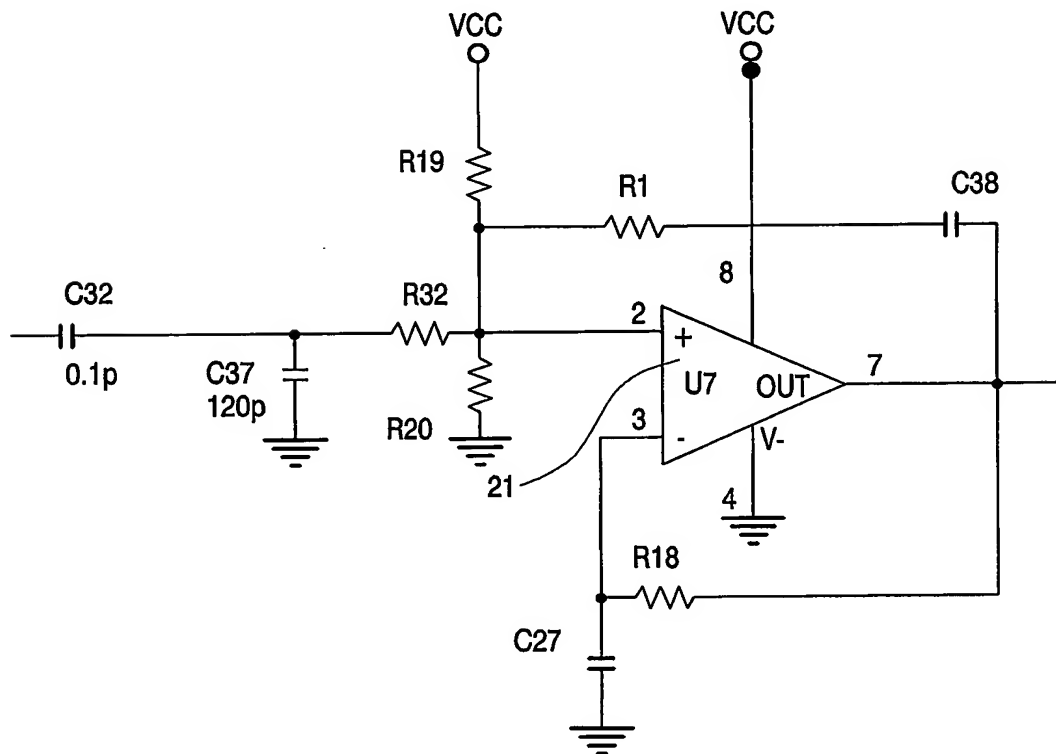


FIG. 2

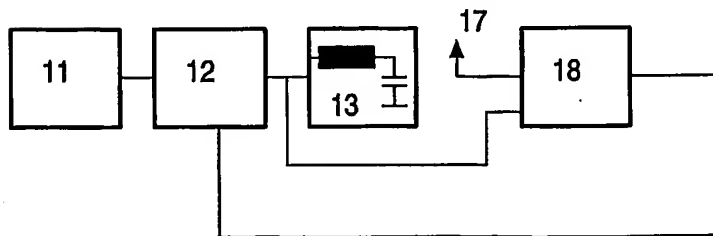


FIG. 4

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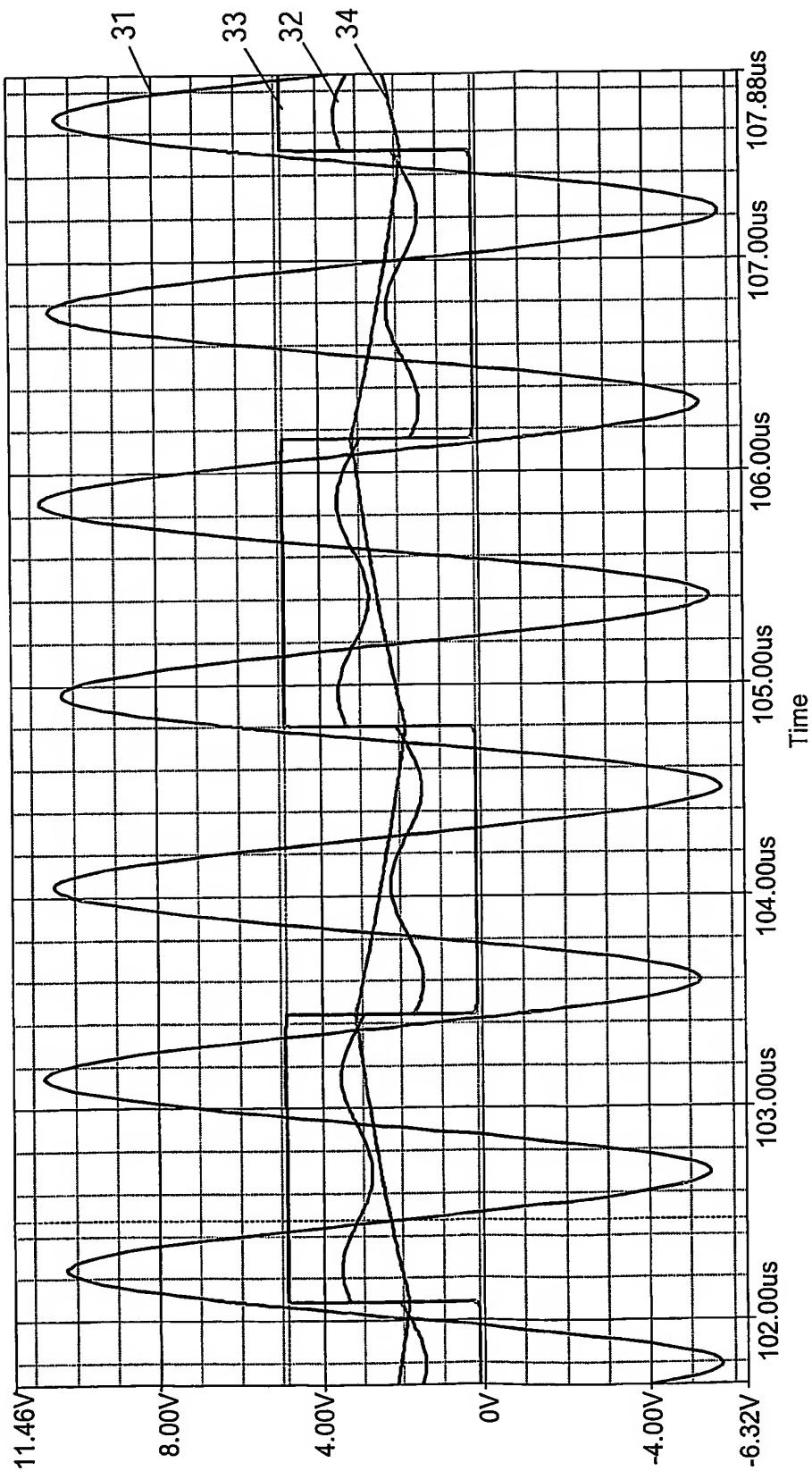


FIG.3

INTERNATIONAL SEARCH REPORT

International Application No
PCT/IB 03/00865

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H05B41/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H05B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 838 117 A (NERONE LOUIS R) 17 November 1998 (1998-11-17) column 2, line 51 -column 7, line 32	1-12
X	US 6 072 710 A (CHANG CHIN) 6 June 2000 (2000-06-06) column 3, line 3 -column 4, line 2	1-12
A	US 4 048 541 A (ADAMS GUY ET AL) 13 September 1977 (1977-09-13) column 2, line 6 - line 36	1-12
A	US 5 502 635 A (BOBEL ANDRZEJ A) 26 March 1996 (1996-03-26) column 6, line 10 -column 8, line 8	1-12
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☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents:

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Date of the actual completion of the international search

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11/06/2003

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INTERNATIONAL SEARCH REPORT

Internat Application No
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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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